

FORM PTO-1449 INFORMATION DISCLOSURE CITATION			Atty Docket BUR9-1999-0264-US1	Serial No. To be assigned 09/588,617			
			Applicant Claude L. Bertin, et al.				
			Filing Date Herewith 06/06/2000	Group Art Unit To be assigned 2829			
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
JN	AA	5,777,484	07/07/98	Woith et al.			
	AB	5,686,843	11/11/97	Beilstein, Jr. et al.			
	AC	5,686,318	11/11/97	Farnsworth et al.			
	AD	5,682,064	10/28/97	Atkins et al.			
	AE	5,678,301	10/21/97	Gochnour et al.			
	AF	5,677,203	10/14/97	Rates			
	AG	5,661,086	08/26/97	Nakashima et al.			
	AH	5,656,945	08/12/97	Cain			
	AI	5,634,267	06/03/97	Farnsworth et al.			
	AJ	5,534,786	07/09/96	Kaneko et al.			
	AK	5,532,612	07/02/96	Liang			
	AL	5,495,179	02/27/96	Wood et al.			
	AM	5,483,174	01/09/96	Hembree et al.			
	AN	5,477,009	12/19/95	Brendecke et al.			
	AO	5,456,404	10/10/95	Robinette, Jr. et al.			
	AP	5,455,518	10/03/95	Sobhani			
	AQ	5,440,241	08/08/95	King et al.			
	AR	5,290,710	03/01/94	Haj-Ali-Ahmadi et al.			
	AS	5,253,415	10/19/93	Dennis			
	AT	5,239,747	08/31/93	Ewers			
	AU	5,173,451	12/22/92	Kinsman et al.			
	AV	5,164,888	11/17/92	Stone, Jr.			
	AW	5,047,711	09/10/91	Smith et al.			
	AX	4,996,587	02/26/91	Hinrichsmeyer et al.			
	AY	4,927,491	05/22/90	Masaki			
	AZ	4,912,547	03/27/90	Bilowith et al.			
	BA	4,701,781	10/20/87	Sankhagowit			
	BB	4,046,985	09/06/77	Gates			
	BC	3,984,620	10/05/76	Robillard et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Trans-lation
	BD						Yes No
	BE						Yes No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
TM	BF	J.C. Peterson and G. Schrottke, "Optical Flex to Multi-Chip Modules", IBM Technical Disclosure Bulletin Vol. 36 No. 12 December 1993					
↓	BG	J.C. Peterson and G. Schrottke, "Flex Interconnect to Multi-Chip Modules", IBM Technical Disclosure Bulletin Vol. 36 No. 12 December 1993					

<i>TR</i>	BH	R.H. Katyl and R.J. Kelleher, "BURN-IN METHODOLOGY FOR TAB USING SEPARATE SIGNAL CARRIER TAPE", IBM Technical Disclosure Bulletin Vol. 32 No. 1 June 1989
Examiner	Date Considered <i>3/23/02</i>	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.		

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) BUR9-1999-026 S1	Application Number 09/588, 617		
				Applicant(s) Bertin et al			
				Filing Date 06/06/00	Group Art Unit 2829		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JW	CA	4,220,917	9/2/80	McMahon, Jr.	_____	_____	_____
JW	CB	4,386,389	5/31/83	Proebsting	_____	_____	_____
JW	CC	4,981,817	1/1/91	Stone, Jr.	_____	_____	_____
JW	CD	5,241,266	8/31/93	Ahmad et al.	_____	_____	_____
JW	CE	5,397,997	3/14/95	Tuckerman et al.	_____	_____	_____
JW	CF	4,441,075	4/3/84	McMahon	_____	_____	_____
JW	CG	5,532,614	7/2/96	Chiu	_____	_____	_____
FOREIGN PATENT DOCUMENTS							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
JW	CH	E. Klink and H. Kohler, Memory Strip Module, IBM TDB, Vol. 33 No. 3B, August 1990, Pg 164 Anonomous author, Flexy-Pak - Three-Dimensional Memory Package, Research Disclosure, March 1990, Number 311, Kenneth Mason Publications Ltd, England					
JW	CI	T. DeStefano and J. Fjelstad, NOvel Uses of Flexible Circuit Technology in High Performance Electronic Applications, Microelectronics International, No 39, January 1996, Pgs 11-15.					
EXAMINER <i>Truefroyce</i>				DATE CONSIDERED <i>3/23/02</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							